

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of )  
TAKAURA et al. )  
Application Number: To Be Assigned )  
Filed: Concurrently Herewith )  
For: A SEMICONDUCTOR MEMORY DEVICE USING )  
VERTICAL-CHANNEL TRANSISTORS )  
Attorney Docket No.: HITA. 0451 )

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

Sir:

Prior to an examination on the merits, please amend the above-identified application as follows: